

IN THE CLAIMS

We claim:

1. A method, comprising:  
etching a waveguide isotropically to smooth a surface of the waveguide.
2. The method of claim 1, wherein the waveguide comprises stoichiometric silicon nitride.
3. The method of claim 1, wherein the waveguide comprises amorphous silicon.
4. The method of claim 1, further comprising etching the waveguide anisotropically before etching the waveguide isotropically.
5. The method of claim 1, wherein etching the waveguide comprises submerging the waveguide in a wet etch solution.
6. The method of claim 5, further comprising applying sonic energy to the wet etch solution while etching the waveguide isotropically.
7. The method of claim 6, wherein the sonic energy is megasonic.
8. The method of claim 7, wherein the megasonic energy is in the approximate range of 800 KHz – 1200 KHz.
9. The method of claim 6, wherein the sonic energy is ultrasonic.

10. The method of claim 9, wherein the ultrasonic energy is in the approximate range of 1 KHz – 50 KHz.
11. The method of claim 5, wherein the wet etch solution comprises an acid compatible with temperatures above approximately 70°C and etches stoichiometric silicon nitride and is selective to dielectric materials.
12. The method of claim 11, wherein the wet etch solution comprises approximately 84% by volume phosphoric acid in water.
13. The method of claim 5, wherein the wet etch solution comprises a base having a pH in the approximate range of 10-13 and etches amorphous silicon and is selective to dielectric materials.
14. The method of claim 13, wherein the base is a non-metallic base.
15. The method of claim 1, further comprising performing the isotropic etch at a temperature in the approximate range of 24°C - 70°C.
16. The method of claim 1, further comprising etching the waveguide for a time sufficient to smooth the surface of the waveguide to maximize retention of a light signal within the waveguide.
17. A method, comprising:
  - forming an amorphous silicon layer on a first dielectric layer;

etching the amorphous silicon layer with an anisotropic dry plasma etch to form at least one waveguide;

submerging the at least one waveguide in an ammonia hydroxide isotropic wet etch solution to which sonic energy is being applied at approximately room temperature for a time sufficient to smooth the a surface of the waveguide; and

forming a second dielectric layer above the at least one waveguide.

18. The method of claim 17, wherein the isotropic etch for amorphous silicon is a wet etch solution comprising ammonium hydroxide in the approximate range of 2% - 10% by volume in water.

19. The method of claim 17, wherein the sonic energy impacts the waveguide with a power in the approximate range of 0.5 W/cm<sup>2</sup> – 10.0 W/cm<sup>2</sup>.

20. A method, comprising:

maximizing retention of an intensity of a light signal within a waveguide by etching a waveguide isotropically to smooth a surface of the waveguide.

21. The method of claim 20, wherein the light intensity loss of a substantially smoothed waveguide is approximately 6 dB/cm.

22. The method of claim 20, wherein the waveguide is amorphous silicon.

23. An apparatus, comprising:

a waveguide on a substrate having a substantially rounded surface.

24. The apparatus of claim 23, wherein the material is amorphous silicon.

25. The apparatus of claim 23, wherein the material is stoichiometric silicon nitride.
26. The apparatus of claim 23, wherein the substrate is a semiconductor wafer.
27. The apparatus of claim 23, further comprising a dielectric layer in between the substrate and the waveguide and a second dielectric layer above the waveguide.
28. An electronic assembly comprising:
  - a package substrate; and
  - a die mounted on the package substrate, the die including a waveguide having a substantially rounded surface.
29. The electronic assembly of claim 28, wherein the waveguide is amorphous silicon.
30. The electronic assembly of claim 28, wherein the waveguide is formed above a silicon substrate.